

PICTURE PROCESSOR

Patent number: JP8123953
Publication date: 1996-05-17
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Classification:
 - international: G09G5/39; H04N7/26; H04N7/50; G09G5/399; G09G5/36; H04N7/26;
 H04N7/50; (IPC1-7): G06T1/60; G06F12/06
 - european: G09G5/39; H04N7/26L2; H04N7/50
Application number: JP19940257075 19941021
Priority number(s): JP19940257075 19941021

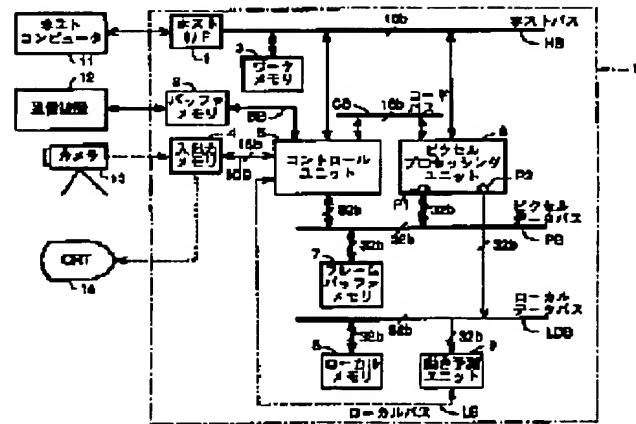
Also published as:

DE19535100 (A1)

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Abstract of JP8123953

PURPOSE: To provide the picture processor which transfers picture element data at a high speed. CONSTITUTION: Each four arrays of picture element data corresponding to four arrays of picture elements arranged in the perpendicular direction of a field picture are stored in banks different from one another out of plural banks in a frame buffer memory 7, and four picture element data adjacent to each other in the perpendicular direction are defined as one data block, and one address is given to this data block. A bank Bank 1 is precharged when a bank Bank 0 is in the write operation, and the Bank 0 is precharged when the Bank 1 is in the write operation; and thereby, the precharge operation and the write operation are performed in parallel to shorten the write time, and picture element data is transferred at a high speed.



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